

### **Memristive Akers logic arrays (Technion)**

**code:** COM-1619

In the coming generations of computing systems, part of the computation is envisioned to be done within memory. Current in-memory techniques suffer from bad integration between logic and memory processes. The presented invention is a hardware version of a modified Akers logic array with memristors which combines memory and logic operations, opening up opportunities for novel non-von Neumann computer architectures, while reducing power and enhancing memory bandwidth. This is especially beneficial for in-memory computing.

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