

**Memristive Akers logic arrays (Technion)****code:** COM-1619

In the coming generations of computing systems, part of the computation is envisioned to be done within memory. Current in-memory techniques suffer from bad integration between logic and memory processes. The presented invention is a hardware version of a modified Akers logic array with memristors which combines memory and logic operations, opening up opportunities for novel non-von Neumann computer architectures, while reducing power and enhancing memory bandwidth. This is especially beneficial for in-memory computing.

**Contact for more information:**T3 Team , 972-4-8294853

---

T - Technion Technology Transfer  
Technion City, Senate Bldg., Haifa 32000, Israel  
Tel. 972-4-829-4851; 972-8325-375  
Fax. 972-4-832-0845