

## Massively multi-threaded computer processor (Technion)

code: COM-1574

Energy consumption has become the primary concern of processor designers and the evaluation by which processors are measured. Because of their energy-efficiency, the GPGPU class of processors is gaining popularity and constitutes a multi-billion dollar market. GPGPUs employ a single-instruction multiple-threads (SIMT) execution model in which a problem is decomposed into multiple threads of execution, all executing exactly the same instructions but operating on different data. The presented invention is a reconfigurable processor design that is completely different from existing GPGPUs and is 2x more power efficient than the current state-of-the-art.

## **Contact for more information:**

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